



Dr. R. JAYAVEL  
DIRECTOR

**CENTRE FOR RESEARCH**  
ANNA UNIVERSITY  
CHENNAI - 600 025



Telephone : +91-44-2235 7366/2235 0381  
Fax : +91-44-2220 1213  
Email : dirresearch@annauniv.edu  
dirresearch@gmail.com



Date : 04.07.2015

Lr.No.CR/Ph.D./Admn/July/2015

To  
Dr.G.A Sathish Kumar  
Professor  
Department of Electronics & Communication Engineering  
Sri Venkateswara College of Engineering  
Sriperumbudur, Kancheepuram - 602 105

Sub : Ph.D. Programme - Provisional Registration - Order - Issued.

Ref : Res. No. 25.16 of the 25th Research Board Meeting held on 27.05.2015.

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I am to inform that **Mr./Ms. Poomagal C T** is permitted to register provisionally for Ph.D. programme as **Full-Time** scholar in the Faculty of Information and Communication Engineering of Anna University during July 2015 session under your guidance.

Topic of Research: **IC Layout Optimization In VLSI Signal Processing Circuits**

The Research Scholar may be instructed to submit the Enrolment Form (accessed through the website <https://cfr.annauniv.edu/scholars/>) within a period of 15 days from the date of issue of this order. The research scholar should submit the Semester Progress Report and Registration Renewal Form in the prescribed format at the time of payment of regular semester fee.

The 50% of the programme fee paid will be refunded, if the candidate withdraws his/her admission within 30 days from the date of payment of fee and thereafter caution deposit only will be refunded in full, subject to conditions.

The Registration Number of the research scholar is **1524499573**.

In all future correspondence quote "**1524499573/Ph.D./AR12**" - for reference. The research scholar will be governed by the "Regulations for Ph.D. Programme" of this University and any amendments are made to the Regulations from time to time.

*[Signature]*  
6.7.15  
F DIRECTOR

To  
1. Individual.  
2. The Head of the Department / Director of the Centre.

*[Signature]*  
4.7.15  
*[Signature]*  
4.7.15